

IGBT Module stack

SEMIKUBE - Size T0.5

3-phase inverter

Ordering No. 08800445

Description IGD-1-424-P1N4-DL-FA
Option 0C 0N 0P K - 0X - 1F2

Features

- Designed in regards to EN50178 recommandations
- RoHS compliant
- Fast mounting and dismounting
- Very high life-time expectancy
- Integrated voltage, current and temperature sensors
- Air cooled power stacks

Typical Applications

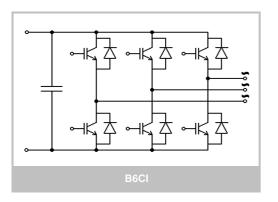
- Industrial applications
- Solar Inverters

Footnotes

1) the user shall ensure that the ambiant air shall be ventilated in order not to create temperature hot spots.

REMARKS

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee, expressed or implied is made regarding delivery, performance or suitability.



Absolute maximun ratings		$T_{AMBIENT} = T_{AIR\ COOLING} = 40^{\circ}C$ unless otherwise specifie		
Symbol	Conditions		Values	Unit
OUT MAX	Maximum continuous output current		200	ARMS
Vout max	Maximum output voltage		500	Vac
VBUS MAX	Maximum DC Bus voltage in operation		900	VDC
Fouт	Inverter Output frequency		500	Hz
Fsw	Maximum switching frequency		25	kHz

	1	$T_{AMBIENT} = T_{AIR\ COO}$		i		i
Symbol	Conditions		min	typ	max	Unit
AC phase						
OUT RATED	Rated output current	V _{BUS} =750V _{DC} , No overload,		200		ARMS
Vout	Output voltage	Tj<150°C, Power factor PF = 1,		400		Vac
Роит	Rated output power	Cabinet airflow in operation at 400m3/h		140		kW
Fsw	Inverter switching frequency	Fan airflow through heatsink at 900	3		kHz	
Fоuт	Output frequency	m3/h		50		Hz
DC Bus						
VBUS	Rated DC voltage			750		VDC
Efficiency						
PLOSS INV	Total power losses				1 915	W
η	Inverter efficiency				>98	%
Filtering cha	racteristics					
VBUS	Rated DC voltage applied to the caps bank without switching				1 100	VDC
VDC CAPACITOR	Max DC voltage applied to the caps bank (max 30% of LTE) without switching				1 100	VDC
$\tau_{d5\%}$	Discharge time of the capacitors (5%)			285		s
CDC	Capacitor bank capacity		1,43		1,68	mF
LTE	Calculated LTE of the caps with forced air cooling				> 100	kH
Stack Insula	tion	<u>.</u>				
Visol	Frame / Power stage AC/DC (insulation test voltage DC, 60s)				3 200	٧



Environmental conditions					
Characteristics	Conditions	min	typ	max	Unit
Climatic		<u> </u>			
Ambient temperature 1)	IEC 60721-3-3, class 3K3 extended In operation	-25		55	°C
Humidity	IEC 60721-3-3, class 3K3 no condensation no icing	5		85	%
Mechanical	-	•			-
Installation altitude	without derating			1 000	m
Protection index	IEC 60529		IP00		-
Pollution degree	EN 50178		2		-
Weight total	3-phase inverter including heatsink fan		25		kg
Thermal data	•	•			
VSUPPLY	Heatsink fan AC voltage supply		230		Vac
PFAN at 50I	Hz Rated power at Vsupply		300		W

SEMIKUBE - Size T0.5

3-phase inverter

Ordering No. 08800445

Description IGD-1-424-P1N4-DL-FA
Option 0C 0N 0P K - 0X - 1F2

Features

- Designed in regards to EN50178 recommandations
- RoHS compliant
- Fast mounting and dismounting
- Very high life-time expectancy
- Integrated voltage, current and temperature sensors
- Air cooled power stacks

Typical Applications

- Industrial applications
- Solar Inverters

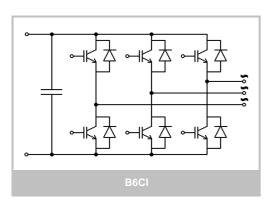
Footnotes

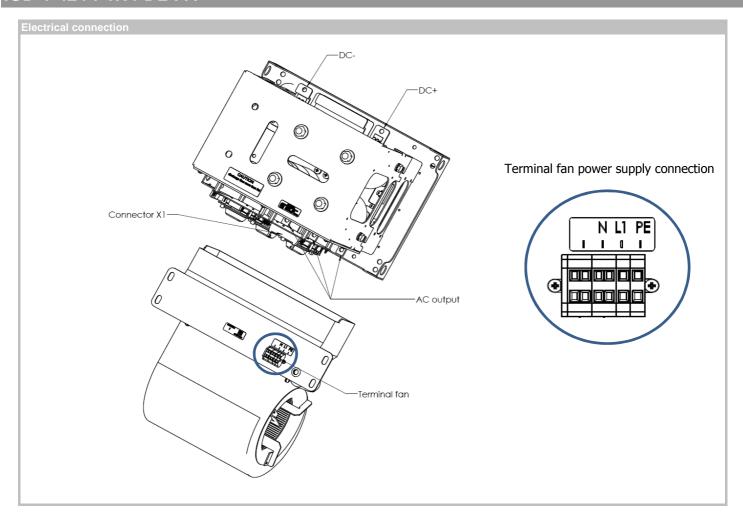
1) the user shall ensure that the ambiant air shall be ventilated in order not to create temperature hot spots.

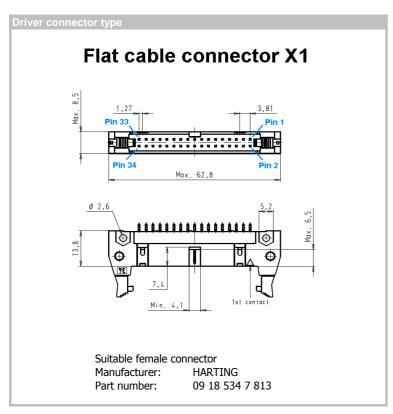
REMARKS

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee, expressed or implied is made regarding delivery, performance or suitability.

Gate Driver Characteristcs		T _{AMBIENT} =25°C unless otherwise specified			
Symbol	Conditions	min	typ	max	Unit
Gate Driver / co	ntroler data				
Vs	supply voltage	21,6	24	26,4	VDC
Iso	Supply primary current No load	360			mA
	Max. Supply primary current			1 500	mA
ViT+	input threshold voltage HIGH			0.7 x V s	VDC
ViT-	input threshold voltage LOW	0.3 x Vs			VDC
Rin	Input resistance		17		kΩ
Cin	Input capacitance		1		nF
Measurement &	protection				-
	Scaling		10		mV.V ⁻¹
DC link voltage	Accuracy of analogue signal @ T _a =25°C	-2		+2	%
sensing	Temperature coefficient			0,03	%.K ⁻¹
U _{DC analogue OUT}	max. load current			5	mA
- Do analogue ou i	Max. voltage range			15	VDC
	Max measurable DC Link Voltage			1 200	VDC
	Scaling		24		mV.A ⁻¹
Current sensing	Accuracy of analogue signal	-4		+4	%
I _{analogue OUT}	Temperature coefficient			0,07	%.K ⁻¹
per phase	Max. output current			5	mA
	Max. voltage range			15	VDC
ITRIPSC	Over current trip level		450		A PEAK
Tomporatura	Scaling		10		mV.°C ⁻¹
Temperature sensing T _{analogue} out	Minimum measurable temperature	25			°C
	Max. output current			5	mA
	Max. voltage range			15	VDC
Ttp	Over temperature protection	95	100	105	°C
Tth	Threshold level for reset after failure event	70			°C

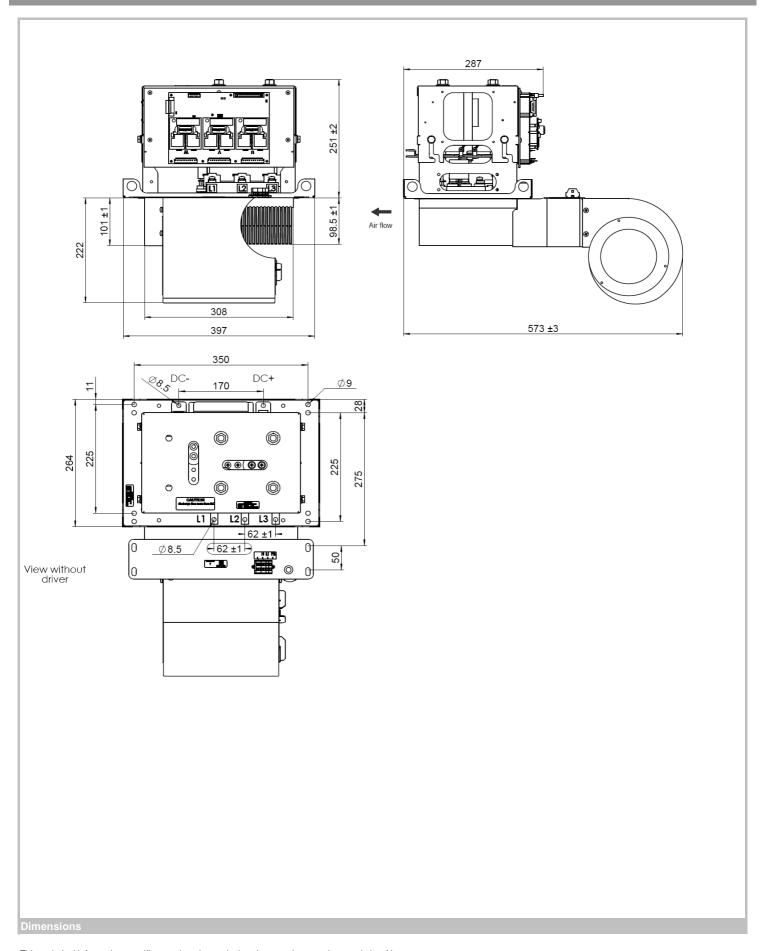






Driver X1 connector assignment

PIN	Signal	Specification
1,3,5	Vs IN	Supply voltage
2,4,6	GND	
7	[Reserved]	[dominant/recessive]
8	GND (Signal Status)	Ground for Signal Status OUT
9	Signal Status BIDIRECTIONAL	24VDC digital logic input, push pull LOW [dominant] = "Not ready to operate" HIGH [recessive] = "Ready to operate"
10	[Reserved]	[dominant/recessive]
11	Temperature Analogue OUT	Nominal voltage range: 010V
12	GND (Temperature Analogue)	Ground for Temperature Analogue OUT
13	UDC Analogue OUT	Nominal voltage range: 010V
14	GND (UDC Analogue)	Ground for UDC Analogue OUT
15	TOP phase U Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
16	BOT Phase U Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
17	[Reserved]	[dominant/recessive]
18	GND (TOP phase U, BOT phase U)	Ground for TOP & BOT phase U IN
19	I phase U Analogue OUT	Nominal voltage range: 010V
20	GND (I Analogue phase U)	Ground for I phase U Analogue OUT
21	TOP phase V Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
22	BOT Phase V Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
23	[Reserved]	[dominant/recessive]
24	GND (TOP phase V, BOT phase V)	Ground for TOP & BOT phase V IN
25	I phase V Analogue OUT	Nominal voltage range: 010V
26	GND (I Analogue phase V)	Ground for I phase V Analogue OUT
27	TOP phase W Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
28	BOT phase W Switching Signal IN	24VDC digital logic input, push pull LOW = "Switch off" HIGH = "Switch on"
29	[Reserved]	[dominant/recessive]
30	GND (TOP phase W, BOT phase W)	Ground for TOP & BOT phase W IN
31	I phase W Analogue OUT	Nominal voltage range: 010V
32	GND (I Analogue phase W)	Ground for I phase W Analogue OUT
33,34	[Reserved]	



This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.

